

# Isolation Trigger Issue

- Lowest Eta bit errors currently at 5-8%, code passes event if `eta_iso != eta_clist`.
- Errors only happen in crate 2, and this bit is next to lowest eta bit on backplane.
- No other known connection between eta and phi in the system.
- This could cause at most 8.5% of non-isolated 18 GeV photons to pass, about 150nb at L2.
- Studied extensively about a year ago.

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- No other board seems to have problems with these bits.
- At the time, happened at the same rate on all 3 Isolists.
- Changing slot didn't help.
- Spent a day with ANL engineers looking at the scope at these two signals, forcing seeds in crate 2. Looked perfect.

# Isolation Trigger Issue

- Problem seemed to go away for 9 months when Greg reseated Isolist for a different reason.
- Could play some games with firmware to help diagnose this time.
- Prefer to do early in shutdown, will be busy with arch work once arch is pulled.